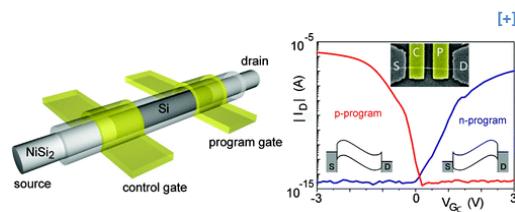


Reconfigurable silicon nanowire transistors

December 22, 2011

Technical University Munich researchers have developed a nanoscale universal transistor that can be configured as p-FET or n-FET simply by applying a voltage.

The design can be flexibly reconfigured during operation to perform different logic computations, allowing for unprecedented circuit design flexibility.



Universal transistor (credit: André Heinzig et al.)

Ref.: André Heinzig et al., Reconfigurable Silicon Nanowire Transistors, *Nano Letters*, 2011 [DOI: [10.1021/nl203094h](https://doi.org/10.1021/nl203094h)]

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